

Notice of Allowability

Application No.

10/726,084

Examiner

Ly D. Pham

Applicant(s)

CHEVALLIER ET AL.

Art Unit

2827

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 31 January 2005.
2. ☒ The allowed claim(s) is/are 1-32 and 34-37.
3. ☒ The drawings filed on 02 December 2003 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


HOAI HO
PRIMARY EXAMINER

DETAILED ACTION

1. Applicant's Response filed January 31, 2005 has been entered and considered.
2. This application is in condition for allowance except the following formal matter.

EXAMINER'S AMENDMENT

3. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Timothy B. Clise (reg. no. 40,957) on March 28, 2005.

The application has been amended as follows:

- i. **Claim 33 is cancelled.**
- ii. **In claims 34 and 35, "The flash memory device of claim 33, ..." is replaced with "The flash memory device of claim 32, ...".**
- iii. **Claim 32 is amended and rewritten as follow:**
 32. A flash memory device comprising:
an array of addressable memory cells arranged in addressable blocks, the addressable blocks comprise a first memory cell location and a second memory cell location;

a first address decoder circuit coupled to the first memory cell location;
and

a second address decoder circuit coupled to the second memory cell location, such that the first and second memory cell locations have two different access times from activation of the flash memory device,

wherein a first voltage pump circuit provides a word line voltage to the first address decoder circuit within a first time period, and a second voltage pump circuit provides a word line voltage to the second address decoder circuit within a second time period which is greater than the first time period.

Allowable Subject Matter

5. Claims 1 – 32 and 34 – 37 are allowed.

6. The following is an examiner's statement of reasons for allowance:

The prior arts teach a flash memory device comprising:

an array of addressable memory cells arranged in addressable blocks, the addressable blocks comprise a boot block and at least one additional memory cell block;

an address decoder circuit couple to the boot block and additional memory cell block; and

a voltage pump circuit coupled to address decoder circuit.

However, the prior arts did not teach the flash memory device, further comprising:

first and second address decoder circuits respectively coupled to the boot block and the additional memory cell block;

a first voltage pump circuit coupled to the first address decoder circuit for providing a word line voltage signal to the boot block; and

a second voltage pump circuit coupled to the second address decoder circuit for providing a word line voltage signal to the additional memory cell block.

7. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.


9. A shortened statutory period for response to this action is set to expire 3 (three) months and 0 (zero) day from the date of this letter. Failure to respond within the period for response will cause the application to become abandoned (see MPEP 710.02(b)).

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10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ly D. Pham whose telephone number is 571-272-1793. The examiner can normally be reached on Monday-Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hoai Ho can be reached on 571-272-1777. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Ly D Pham 
March 28, 2005


HOAI HO
PRIMARY EXAMINER